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Docket No. NEC00P264-KS

2823/

## In re patent application of

Kenichiro Nakagawa

Serial No.: 09/688,203

Group Art Unit: 2823

Filed:

October 16, 2000

**Examiner:** William M. Brewster

For:

METHOD OF MANUFACTURING SEMICONDUCTOR MEMORY DEVICE INCLUDING ONE STEP OF FORMING EXPOSING THE SURFACE OF THE SELECT TRANSISTORS WHILE NOT EXPOSING

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

THE SURFACE CELL TRANSISTORS

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents, cited in the Japanese Office Action, and listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

In compliance with the requirements of 37 CFR §1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designated in 35 U.S.C. §1.56(c) most knowledgeable about the content of the information, the undersigned attorney of record submits a translation of portions of an official action by a foreign examiner in which the references were cited. The relevance to the pending U.S. patent application is that the references were cited in a foreign patent application on the same subject matter. However, no independent analysis of the references, the accuracy of the statement of the foreign examiner or the claims of the foreign application under the laws of that country of the United States relative to the subject matter claimed in the present application has been made, the present understanding of the contents thereof by the undersigned being based on the translation of the foreign examiner's comments submitted herewith.

## Docket No. NEC00P264-KS

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that each item of information contained in this Information Disclosure Statement was the first citation of that item by a foreign patent office in a counterpart foreign application, which occurred not more than three months prior to the filing of this statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Submitted herewith is a check in the amount of \$180.00 to cover the Information Disclosure Statement fee.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

Frederick E. Cooperrider Registration No.: 36,769

Date: December 16, 2003 McGinn & Gibb, PLLC Intellectual Property Law 8321 Old Courthouse Road, Suite 200 Vienna, Virginia 22182-3817 (703) 761-4100

Customer No. 21254

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Docket Number (Optional) NEC00P264-KS		Application Number 09/688,203		
				Applicant(s)				
				Kenichiro NAKAGAWA Filing Date Group Art Unit				
				October 16, 2000		2823		
U.S. PATENT DOCUMENTS								
EXAMINER REF P	DOWNENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING IF APPRO	18
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FOREIGN PATENT DOCUMENTS								
REF	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	Trans YES	lation NO
11-126834		05/11/99	JAPAN				Abs.	
10-270578		10/09/98	JAPAN				Abs.	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
Japanese Office Action dated November 5, 2003, with partial English translation.								
EXAMINER				DATE CONSIDERED				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								